

FIG. 2

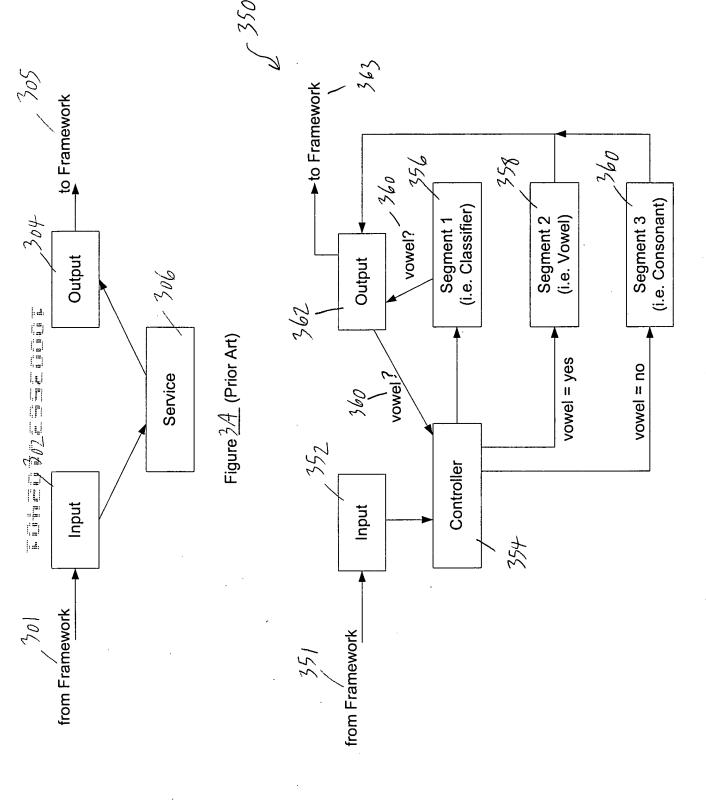


Figure  $\overline{\mathcal{IB}}$ 

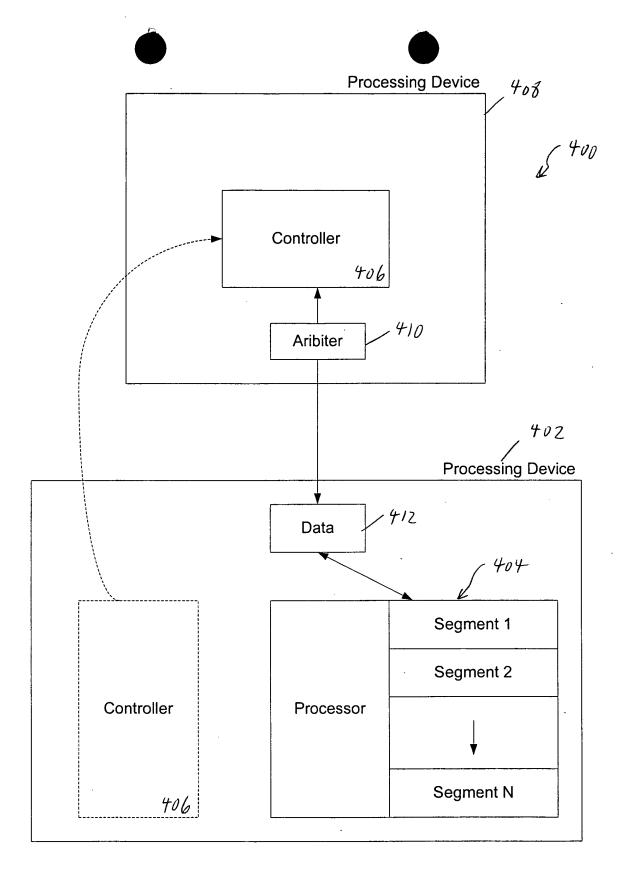


Figure <u>4</u>

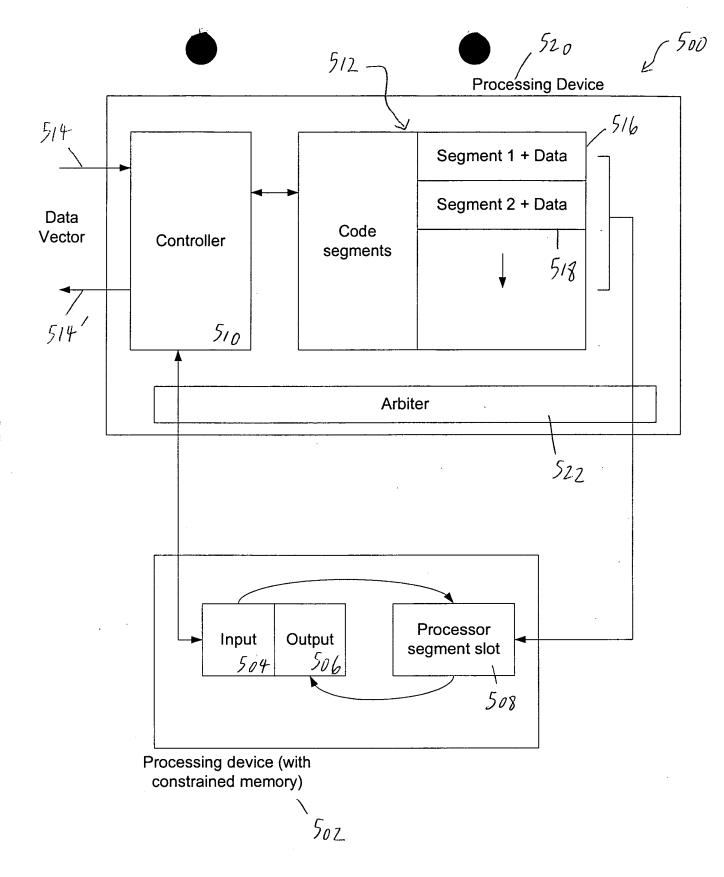


Figure <u>5</u>

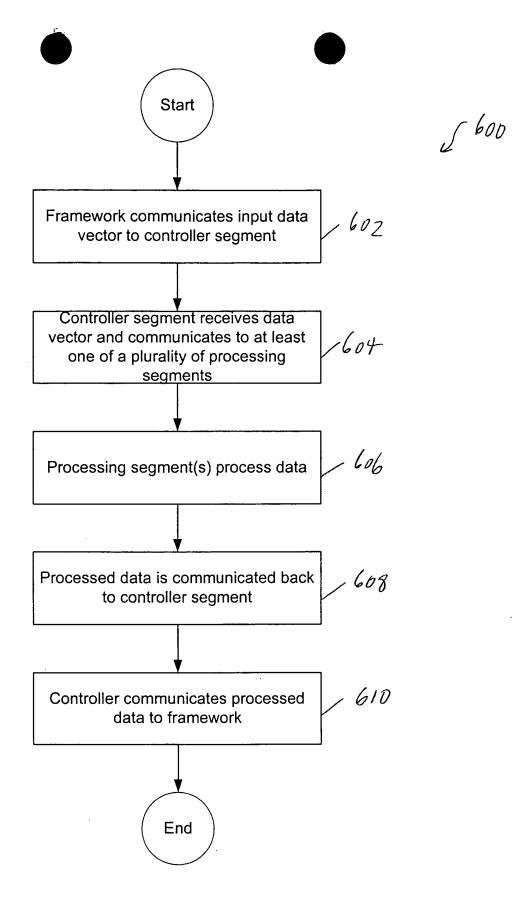


FIG. 6

5 700

702

704

106

108

FIG. 7

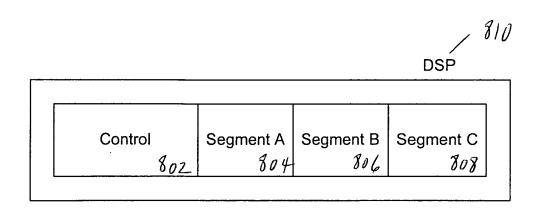


FIG. 8A

MCU

Control Segment A Segment B Segment C 802 804 806 808

FIG. 8B

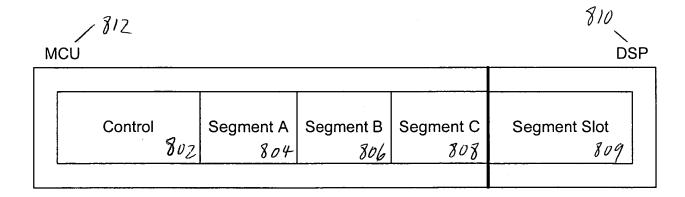


FIG. 8C

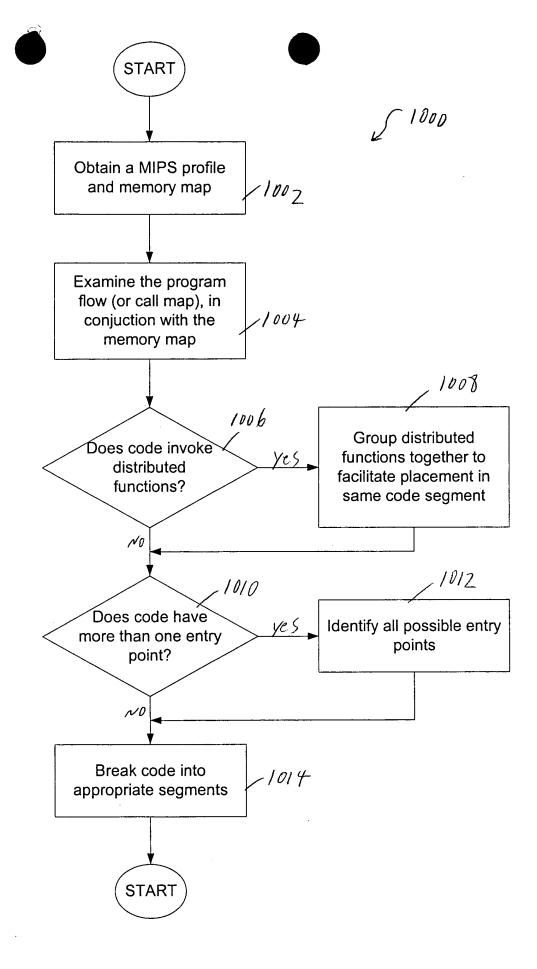


FIG. 10

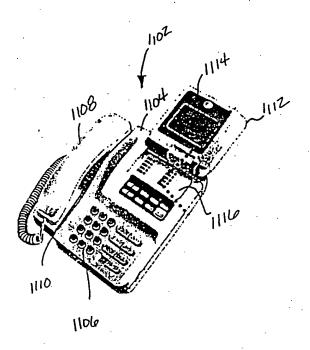


FIG. 11

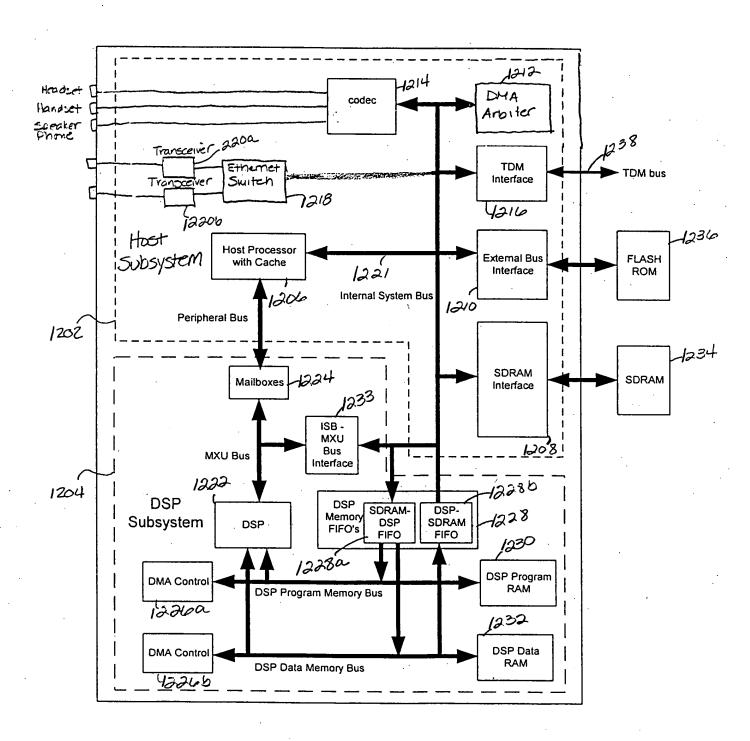
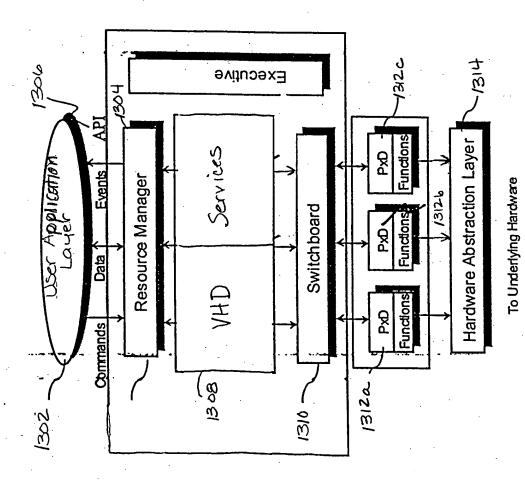


FIG.12



F16.13

